# SAINTERNATIONAL SEMICONDUCTOR CONFERENCE

#### IN PARTNERSHIP WITH



Department for Science, Innovation, & Technology



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- <u>Stewart Edmondson</u>
- Tamsin Heath
- Vaysh Kewada
- Will Drury
- Yankin Tanurhan







#### **TSMC Core Values**

#### Integrity

Integrity is our most basic and most important core value. We tell the truth. We do not make commitments lightly. Once we make a commitment, we devote ourselves completely to meeting that commitment.

#### Commitment

TSMC is committed to the welfare of customers, suppliers, employees, shareholders, and society.

#### Innovation

Innovation is the wellspring of TSMC's growth, and is a part of all aspects of our business, from strategic planning, marketing and management, to technology and manufacturing.

#### **Customer Trust**

At TSMC, customers come first. Their success is our success, and we value their ability to compete as we value our own. We strive to build deep and enduring relationships with our customers, who trust and rely on us to be part of their success over the long term.



GSA is Where Leaders Meet to establish a profitable and sustainable semiconductor ecosystem. This expanding ecosystem encompasses semiconductors, software, solutions, systems and services. As a leading semiconductor and technology industry organization, we offer an efficient and strategic platform for thought leadership.

GSA has an impressive global footprint representing 300+ corporate members on six continents. As a result of our unique, neutral platform, our membership ranges from the most exciting, emerging companies to semiconductor industry stalwarts and technology leaders. Our members now represent 75% of the \$550B plus semiconductor industry.



#### WELCOME MESSAGE



JODI SHELTON
Co-Founder and CEO
GSA

Welcome to the International Semiconductor Conference, co-hosted by the Global Semiconductor Alliance (GSA) and the UK Government's Department for Science, Innovation & Technology (DSIT).

It is a privilege to have you all gathered here in London as we embark on an insightful journey into the transformative power of semiconductor innovation. GSA provides a unique, neutral platform for global collaboration, much of which occurs at curated events such as this, where global leaders interface and innovate with peers, partners, and customers.

Throughout these two days, we are honored to host an esteemed gathering of senior business leaders, investors, and public policy officials from across the globe. Together, we will delve deep into the latest trends shaping the semiconductor industry and assess the current economic and geopolitical landscape.

As the agenda is packed with discussions that promise to be both enlightening and impactful, let us seize this opportunity to exchange ideas and forge meaningful connections. We also owe a very gracious thank you to today's sponsors who have made this event possible. We are truly grateful for your partnership.

Lastly, we would like to thank you for joining us and making the inaugural International Semiconductor Conference a success! We hope you find this event as rewarding and beneficial as we have planned.





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#### **PROGRAM**

#### WOMEN IN SEMICONDUCTORS CONFERENCE

PRE-EVENT - WEDNESDAY, MARCH 13TH

10:00 - 10:15	Welcome to WLI EMEA Women in Semiconductors Jodi Shelton Co-Founder and CEO, GSA
10:15 - 10:30	Keynote: A Recipe for Success Maria Marced Board Member, Ceva, IQE, Sequans Former President, TSMC Europe
10:30 - 10:50	Keynote: Adjusting Perspectives Françoise Chombar Chairwoman and Co-Founder, Melexis
10:50 - 11:30	Panel - Pathway to Success
	Moderator: Giulietta Poltronieri Partner, McKinsey & Company
	Panelists: Janet Collyer Board member at EnSilica, Quantum Dice, Machine Discovery, Aerospace Technology Institute UK Semiconductor Advisory Panel
	Frédérique Le Grèves President, STMicro France EVP, Europe Public Affairs, STMicroelectronics
	Catherine Ramsdale SVP Technology, Pragmatic
	Paul Williamson SVP & GM, IoT Line of Business, Arm
11:30 - 11:45	UKESF "Girls into Electronics" Initiative Stew Edmondson CEO, UK Electronics Skills Foundation
11:45 - 12:00	Closing Remarks
12:00 - 13:00	Networking Lunch



#### PROGRAM

#### WEDNESDAY, MARCH 13TH

13:00	Opening Session
13:00 - 13:15	Welcome Remarks Jodi Shelton, Co-Founder and CEO, GSA
13:15 - 13:30	Opening Remarks Jalal Bagherli, Co-Chair, UK Semiconductor Advisory Panel
13:30 - 13:55	UK National Semiconductor Strategy Goals Saqib Bhatti, UK Minister for Tech and the Digital Economy
13:55 - 14:10	Industry Outlook and the Role of Governments  Al and Industrial Policies - Transforming Our Industry Structure Christopher Thomas, Founder, Integrated Insights
14:10 - 14:50	Networking Break
14:50 - 15:40	Panel - The Role of Governments in the Semiconductor Industry Moderator: Peter Kuo, Industry Partner, Novo Tellus Capital Partners
14:50 - 15:40	Industry Moderator:
14:50 - 15:40	Industry Moderator: Peter Kuo, Industry Partner, Novo Tellus Capital Partners Panelists:
14:50 - 15:40	Industry Moderator: Peter Kuo, Industry Partner, Novo Tellus Capital Partners  Panelists: Daniel Armbrust, Co-founder, Silicon Catalyst
14:50 - 15:40	Industry Moderator: Peter Kuo, Industry Partner, Novo Tellus Capital Partners  Panelists: Daniel Armbrust, Co-founder, Silicon Catalyst  Frank Bösenberg, Managing Director, Silicon Saxony  Maria Marced, Board Member, Ceva, IQE, Sequans & Former







#### Innovation

Our expertise in process control and process-enabling solutions makes us an ideal partner for developing semiconductor technologies.

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- Defect inspection and review
- Metrology
- Wafer etch and deposition
- IC substrate processes
- In situ process management
- Software solutions
- Chemistry process control

#### PROGRAM

#### WEDNESDAY, MARCH 13TH

15:40 - 16:25	Semiconductor Technologies Impacting Net-Zero
15:40 - 15:55	Towards NETZERO for the IC Industry Sebastian Göke, Associate Partner, McKinsey & Company
15:55 - 16:10	New Materials Innovation & the Future of Semiconductor Technologies Simon Thomas, CEO, Paragraf
16:10 - 16:25	Sustainability Use Cases Supported by Low Carbon Semiconductor Manufacturing David Moore, CEO, Pragmatic
16:25 - 17:05	Networking Break
17:05 - 17:20	GaN Solution for Energy Efficient Power Electronics Giorgia Longobardi, Founder and CEO, Cambridge GaN Devices
17:20 - 17:35	Third Generation Semiconductors: Enabling AI in a Net-Zero World Americo Lemos, CEO, IQE
17:35 - 18:15	Panel - Semiconductor Technologies impacting Net-Zero Moderator: Sebastian Göke, Associate Partner, McKinsey & Company  Panelists: Americo Lemos, CEO, IQE Giorgia Longobardi, Founder and CEO, Cambridge GaN Devices David Moore, CEO, Pragmatic Simon Thomas, CEO, Paragraf
18:15 - 18:25	Day 1 Closing Remarks
18:25 - 19:25	Networking Reception



#### PROGRAM

#### THURSDAY, MARCH 14TH

9:00 - 9:10	Welcome Remarks Sandro Grigolli, Vice President & EMEA Executive Director, GSA
9:10 - 10:15	Al and Hyperscalers
9:10 - 9:20	Chiplet-based Architectures for Compute - An Ultimate Solution for Europe Philippe Notton, CEO and Founder, SiPearl
9:20 - 9:30	The Next Generaton of Networking in AI Hardware: Is Optics the Solution? Vaysh Kewada, CEO, Salience Labs
9:30 - 9:40	Bulding the Future of Computing in the Age of Al Paul Williamson, SVP & GM, IoT Line of Business, Arm
9:40 - 10:15	Panel - AI & Hyperscalers Moderator: Daniel Armbrust, Co-founder, Silicon Catalyst
	Panelists: Paul Williamson, SVP & GM, IoT Line of Business, Arm Philippe Notton, CEO and Founder, SiPearl Vaysh Kewada, CEO, Salience Labs
10:15 - 11:05	Networking Break
11:05 - 12:10	Electrification and Advancements in Automotive
11:05 - 11:20	Semiconductors in Electrification Will Drury, Executive Director, Digital & Technologies, Innovate UK
11:20 - 11:35	Advancements in Automotive Semiconductors and Sustainability Challenges Amandeep Bhullar, Global Director – Semiconductor Engineering, Jaguar Land Rover
11:35 - 11:50	High Reliability Materials for Automotive Semiconductor Devices Oreste Donzella, EVP, KLA
11:50 - 12:05	Session Q & A Moderator: Will Drury, Executive Director, Digital & Technologies, Innovate UK
12:05 - 12:10	Closing Remarks



## J.P.Morgan

As a leading investment banking partner to semiconductor firms globally, J.P. Morgan is proud to sponsor the

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#### PROGRAM

#### THURSDAY, MARCH 14TH

12:10 - 13:10	Networking Lunch
13:10 - 14:45	Semiconductor Industry Outlook and Investment Landscape
13:10 - 13:25	Industry Outlook – Fundamentals, Valuations and M&A Matt Gehl, Managing Director, Head of EMEA Tech Banking, JPMorgan Chase & Co.
13:25 - 14:00	Panel - Semiconductor Investment Perspective Moderator: Dennis Atkinson, Investor, BGF  Panelists: Ian Lane, Partner, CIC (Cambridge Innovation Capital) Jeremy Gleeson, Portfolio Manager & Head of Investment Team, AXA Investment Managers
	Niranjan Sirdeshpande, Head of Investment Catalyst Funds, M&G
14:00 - 14:15	New Trends in IP Yakin Tanurhan, SVP Engineering, Synopsys
14:15 - 14:45	Panel - Intellectual Property Benefits and Challenges Moderator: Andrew Thompson, Partner, UK and European Patent Attorney, EIP  Panelists: Anne McAleer, IP Manager, Paragraf Audrey Stone, Board Advisor and Founder, Blueshift Memory lan Lane, Partner, CIC (Cambridge Innovation Capital)
14:45 - 15:20	Networking Break



#### PROGRAM

#### THURSDAY, MARCH 14TH

15:20 - 16:30	UK Semiconductor Startup Showcase
15:20 - 15:30	Fundraising 101 for Semiconductor Startups Pete Hutton, Chairman of Agile Analog, Cambridge GAN Devices
15:30 - 16:30	ChipStart UK Startup Showcase Sean Redmond, Managing Partner, Silicon Catalyst
	UK Startups
	<ul> <li>Rahul Tyagi, CEO, SECQAI</li> <li>Rodolfo Rosini, CEO, Vaire Computing</li> <li>Tanyaradzwa Mangoma, Founder, HyperCIM</li> <li>James Lewis, CEO, RED Semiconductor</li> <li>Shahida Imani, CEO, Singular Photonics</li> <li>Helen Duncan, Marketing Director, Blueshift Memory</li> <li>Dorian Haci, CEO and Co-Founder, MintNeuro</li> <li>James Lee, CEO, Wave Photonics</li> <li>Noel Hurley, CEO, Literal Labs</li> <li>Mark Rushworth, CEO, Finchetto Ltd.</li> </ul>
16:30 - 16:45	Closing Remarks
16:45 - 17:45	Reception









In collaboration with Capgemini, GSA has recently unveiled our latest interest group: The Sustainability Interest Group.

This group serves as a global platform for executives and experts to collaborate and work towards fostering a sustainable semiconductor ecosystem. It provides a neutral forum where participants can engage with peers, partners, suppliers, and customers to address key challenges and explore opportunities. Through collaborative dialogue, the group aims to drive sustainability initiatives within the industry.

The focus of this workshop is to identify the prevailing challenges encountered by companies across the semiconductor value chain. Attendees will acquire insights and resources and will facilitate a roundtable discussion to collectively tackle these challenges and progress towards a more sustainable future.





AMANDEEP BHULLAR

Global Director – Semiconductor Engineering

Jaguar Land Rover

Amandeep is a leading semiconductor strategist in the automotive industry with 24+ years of experience in semiconductor, consumer electronics and automotive industries. He is known for his robust technical knowledge in semiconductor design and manufacturing, advancement in semiconductor technologies, semiconductor supply chain and sustainability, autonomous vehicles, and software defined vehicle architecture. Having worked in personal computing, mobile communication, and automotive industries when advancements in semiconductor technology transformed these industries and the user experience, he actively participates in creating end-to-end strategy for delivering industry leading products and features. Amandeep works with industry leading semiconductor companies for creating a roadmap of advanced semiconductors for automotive applications and software defined vehicles.

Amandeep has a master's degree in electrical engineering from Panjab University, Chandigarh, India.



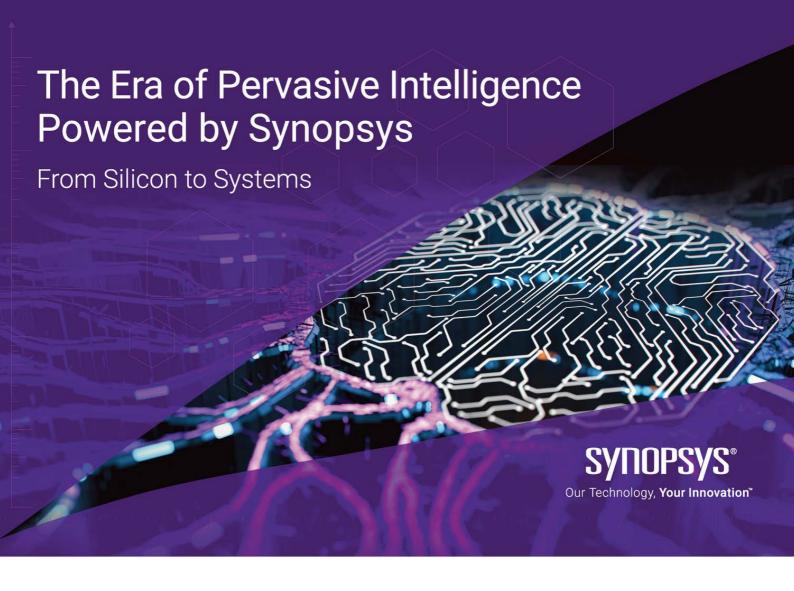


AMERICO LEMOS
CEO
IQE

Americo Lemos joined IQE, the leading global supplier of advanced wafer products and material solutions in January 2022 from the executive team at GlobalFoundries, where he was the Senior Vice President of Business Development for Asia Pacific and China Country President. Americo was responsible for driving the business's efficiency and growth in these critical markets. Prior to this, he was Senior Vice President at Qualcomm, responsible for its data center business. Before joining Qualcomm, Mr Lemos was Vice President of Platform Engineering at Intel, responsible for strategic ventures with China semiconductor companies from 2009 to 2015. Before Intel, he held leadership roles with Texas Instruments, Quanta Computer in Taiwan and Skyworks.

Mr Lemos holds a Master of Sciences, Electronics and Computers degree from École Nationale Supérieure de Sciences Appliquées et de Technologie (ENSSAT) in Lannion, France.





## Our **future** starts with **you**



Imec is an independent world leader in nanoelectronics R&D and is convinced that **diversity** is the main driver of innovation. Through technology, we tackle challenges that impact the way we live. Imec is committed to being a workplace that creates a **sense of belonging for everyone**. A place where everyone can be the **best version of themselves**.



ANDREW THOMPSON
Partner
EIP

Andrew is a leading semiconductor specialist patent attorney and IP strategist, known for his robust technical background in electronics and semiconductor technologies. Andrew boasts comprehensive experience in guiding tech companies through invention capture, patent filing and prosecution and third-party patent rights.

Andrew uses his deep industry knowledge and understanding of government policies to advise on practical IP strategies. He actively contributes to thinking on semiconductor IP strategy and the evolving semiconductor patent landscape through written articles and speaking engagements on these topics.





ANNE MCALEER
Head Of IP
Paragraf

Anne has worked in the development, management and licensing of Intellectual Property in semiconductors and mobile communications for more than 20 years, in start-ups and in large corporations.

Anne is responsible for Paragraf's strategy to maximise the value of its IP in graphene electronics. Paragraf's ground-breaking deposition technology enables manufacture of graphene devices at scale.

Anne's prior roles include Senior IP Director for IDEX Biometrics and Patent Manager at UK semiconductor company CSR, overseeing its large portfolio of Bluetooth/WiFI/GPS patents and supporting M&A, including sale of CSR's handset chip unit to Samsung and sale of CSR to Qualcomm. Anne was also a product manager at UbiNetics, a 3G-chipset developer, and a technology consultant at PA Consulting Group.





AUDREY STONE
Board Advisor and Founder
Blueshift Memory

Dr Audrey Stone is a technology lawyer and entrepreneur with over 18 years of experience in the fields of IP, data privacy, M&A, and international licensing. She currently advises Blueshift Memory, a Cambridge based fabless semiconductor IP start-up that has developed a novel high-speed computer architecture addressing the memory bottleneck in data-intensive applications. She is also the founder and consultant of Bliss Business Solutions, a business consultancy firm that provides a one-stop-shop project management model to SMBs. She has worked with various multinational enterprises and emerging technology companies as an in-house counsel or a seconded lawyer.

As a Founder and former CEO of Blueshift Memory, Dr Stone has helped the company to secure multiple patents, assemble an Advisory Board consisting of semiconductor veterans, secure a UK Smart Innovation Grant and partner with leading semiconductor industry players and academic institutions. She leverages her extensive legal and business expertise to help clients protect and monetize their IP assets, develop and execute global merchandising strategies and organisational governance structures and comply with global business laws and best practices. Audrey has published multiple articles on innovation, data protection, and digital transformation in the entertainment sector. She is passionate about creating value and impact through technology and law.





CATHERINE RAMSDALE
SVP, Technology
Pragmatic Semiconductor

Dr Catherine Ramsdale is Senior Vice President of Technology at Pragmatic Semiconductor and has over 20 years' experience in printed electronics R&D. She has pioneered technologies for ultra-thin semiconductor circuitry on flexible substrates, including successful transfer to manufacturing.

Catherine holds a PhD in organic electronics (Cambridge) and is a named inventor on more than 20 inventions, leading to over 100 patent filings.





CHAOYIN CHI
Deputy General Director of the ISTI
Industrial Technology Research Institute

Chaoyin Chi is the Deputy General Director of the Industry, Science and Technology International Strategy Center (ISTI) at the Industrial technology Research Institute (ITRI). With over 20 years of experience, she has been working as an industry analyst in the electronics field, focusing on government policy planning, long-term forecasting of technology development, and business transformation strategy for private companies.

Her research expertise lies in digital convergence, wireless communications, artificial intelligence, and semiconductors. Chi leads the industry analyst team at ITRI and has lectured at numerous industry seminars.





CHRIS THOMAS
Founder
Integrated Insights

Chris Thomas is the founder of Integrated Insights, an advisory firm serving the Boards, Chairpersons and CEOs of global companies; a Visiting Professor at Tsinghua University; a non-resident Senior Fellow in Artificial Intelligence and Emerging Technologies at the Brookings Institution; and a member of the Council on Foreign Relations.

Chris spent ten years as a Partner at McKinsey & Company, leading various efforts including the Asia Semiconductor Practice, the Global Digital Strategy Practice, and the Global IOT service line. McKinsey ranked Chris among the top 10% of global partners in three of his final four years at the Firm. While at McKinsey, Chris founded the CEO Circle, a gathering of top C-suite executives from Chinese, American, and European companies in the technology industry.

Prior to McKinsey, Chris spent a decade at Intel. He was the General Manager of Intel China, the largest semiconductor business in China. He owned the region's \$5 billion-plus P&L and grew revenues by more than 50% within 3 years. He led China's successful elevation from a country-level sales unit to an independent regional P&L reporting directly into headquarters; directed Intel's Olympic sponsorship campaign; and directly managed a senior team of over 300 local employees.

Chris also held multiple executive roles at Intel's global headquarters. These roles included Chief of Staff to Intel's Chief Sales, Marketing, and Strategy Officer, where he advised on all key decisions for Intel's 5,000-person global salesforce. As GM of the optical products business unit, he led large engineering teams in the design, manufacture, and successful launch of three advanced optical networking semiconductors. During his Intel career, Chris delivered more than \$20 billion in revenue and \$10 billion in gross margin to the company.

Chris began his career as a private equity investor at The Blackstone Group in New York City and has served as a Board Director at Velodyne, the inventor of LiDAR. Chris is recognized as a leading global authority on the global semiconductor industry and the impact of US and Chinese technology policies. In 2021, the State Council of the People's Republic of China recognized Chris as a national-level foreign expert in science and technology. He received an MBA from Stanford Business School, where he was an Arjay Miller Scholar; a Master of Arts in Political Science from Stanford University; and a Bachelor of Science in Economics, summa cum laude, from the Wharton School.





DANIEL ARMBRUST
Co-Founder
Silicon Catalyst

Daniel Armbrust is a Silicon Catalyst co-founder, member of the Board of Directors and its initial CEO.

Daniel is also currently working with the Lawrence Berkeley National Labs to define a strategic public-private partnership program to continue advances in computing and semiconductor technologies. He is currently on the Board of Ayar Labs, a silicon photonics startup and graduate of Silicon Catalyst.

He served as President and Chief Executive Officer of SEMATECH from 2009 to 2014, with the responsibility to lead the consortium's advanced technology R&D and manufacturing collaboration programs. Armbrust previously held various technical, management and executive positions for over 25 years at IBM, culminating in his tenure as Vice President of 300mm semiconductor operations.

Armbrust earned a bachelor's degree in ceramic science and engineering from Pennsylvania State University as well as a master's of science degree in manufacturing systems engineering from Rensselaer Polytechnic Institute.





DAVID MOORE
CEO
Pragmatic Semiconductor

Dave has over 25 years semiconductor leadership experience. Most recently, he was Chief Strategy Officer at Micron Technology, Inc where he had group-wide accountability for a range of corporate functions. Prior to Micron, he spent 6 years at Intel in a number of executive leadership roles, including General Manager Programmable Solutions Group, leading the multi-billion-dollar FPGA and Structured ASIC business.

Dave has a BS in Electronic Engineering from University College Dublin and a MS in Electronic Engineering from the Georgia Institute of Technology. He has lived and worked in many countries including Malaysia, Vietnam, Ireland, the US and UK.





DENNIS ATKINSON
Investor
BGF Investment Management

Dennis joined BGF in 2021, based in London and with a focus on investments across climate and deeptech. He has been active in venture capital for over 20 years, as an investor across Europe, the US and Asia for Softbank and DFJ ePlanet Capital. Dennis has a physics background, beginning his career working on global information systems in the earlier days of the Internet before moving into venture capital and has been party to over 100 transactions since.

He holds a First Class Honors Bachelor of Science Degree in Physics from Exeter University, and a Master of Business Administration from Harvard Business School.





FRANK BÖSENBERG

Managing Director

Silicon Saxony

Frank Bösenberg graduated from TU Dresden in civil engineering and business administration before starting to work in the field of international project management, mainly related to EU funded R&D projects. In 2014, he joined Silicon Saxony as project manager of Silicon Europe, the European network of semiconductor clusters.

Since 2018, he is Managing Director of the Dresden based network organisation that supports its more than 400 members with regard to networking, lobbying and consequently growth, thereby actively contributing to the ambitious goals of the EU Chips Act.





FRANCOISE CHOMBAR
Co-Founder & Chair, Board of Directors
Melexis

Françoise Chombar is Chairwoman and co-founder of Melexis. She served as the CEO of Melexis for 18 years (from 2003 to 2021). She is currently a member of the Board of Umicore, Soitec, Smart Photonics, Antwerp Management School and Mediafin.

She is also president of the STEM platform, an advisory board to the Flemish government, aiming to encourage young people to pursue a Science, Technology, Engineering, or Mathematics education. Françoise Chombar's long-term commitment to actively advocating more STEM and gender balance is driven by her profound belief in their positive societal impact.

She is the recipient of numerous awards including the Vlerick Award, the Global Prize for Women Entrepreneurs of BNP Paribas, ICT-Personality of the Year by Datanews, Science Fellowship at the VUB, Honorary Award by the Flemish Community, the Computable Lifetime Achievement Award, a Honorary Award by the Science and Technology Group of KU Leuven and the European SEMI Award.

Ms. Chombar holds a Master's Degree Applied Languages (Dutch, English, and Spanish) from Ghent University.





# FRÉDÉRIQUE LE GREVÈS President, STMicroelectronics France / EVP, Europe & France Public Affairs STMicroelectronics

After a career in the automotive industry (Delphi, Nissan, Renault, Mitsubishi), in general management and communications and marketing management positions with responsibilities on an international scale, Frédérique Le Grevès turned to towards Business Angel activity in January 2020.

Experienced in brand management and sensitive to multicultural intelligence, Frédérique brings her skills and experience to entrepreneurs. Frédérique is today and since March 2021 the CEO of STMicroelectronics France and Executive Vice President for Public Affairs France and Europe.





GIORGIA LONGOBARDI
Founder & CEO
Cambridge GaN Devices

Dr. Giorgia Longobardi is the co-founder and CEO of CGD since 2016. She is an experienced engineer with international practice working on GaN power devices. As the inventor of high impact patents in the field of GaN power devices, Giorgia has the unique blend of academic and business know-how as one of her biggest strengths.

During her PhD in power devices at Cambridge University, Giorgia worked on international projects with top semiconductor companies, through which she learned about different cultures operating in this field. So far, she has raised \$30mil and she currently leads a team of 60 employees located in five different countries across 3 continents with the aim to deliver to market the most energy efficient power device technology.





GIULIETTA POLTRONIERI
Partner
McKinsey & Company

Giulietta Poltronieri is a Partner in McKinsey's Global Semiconductors and Advanced Electronics practices. She leads transformational programs with semiconductor clients along the full value chain, including semiconductor equipment players, IDMs, fabless, foundries, OSATs, and customers, with focus on operations, strategy and people topics.

She is leading McKinsey's "Talent in Semiconductors" Global Service Line and is the EMEA "All-in, Diversity & Inclusion" Leader for McKinsey Advanced Industries practices, leading initiatives to attract and retain female talent in sectors where the male gender has traditionally been prevalent.

Giulietta holds a double Master in International Management from Bocconi University and HEC Paris. Before joining McKinsey, she has briefly worked at Google in the role of Business Associate.





IAN LANE
Partner
Cambridge Innovation Capital

Before joining CIC Ian was a Director at Unilever Ventures in London, where he led all B2B/Technology investments. Over 11 years he was responsible for numerous transactions across Europe and North America, including investments into Olapic (acquired by Monotype Inc), Mirakl, Aera Technology, Skupos, WeGift, Celtra (acquired by STG), CreatorlQ, Mirriad (Listed on AIM) and Blis Global (acquired by LDC). Prior to Unilever Ventures, Ian worked at L.E.K. Consulting in London and Australia, working on strategy consulting and private equity duediligence projects.

lan has a PhD in Physical Chemistry from the University of Cambridge and MSci in Chemistry from Bristol University.

Ian manages CIC's investment in Seldon, Cambridge GaN Devices, Salience Labs, Unlikely AI, AudioTelligence and Secondmind.





JALAL BAGHERLI
Co-Chair
UK Semiconductor Advisory Panel

Dr. Jalal Bagherli is a visionary leader of the semiconductor industry who created multiple billion-dollar businesses. Currently Dr. Jalal chairs the board of PTSL, a private high growth semiconductor manufacturing company, cochairs the UK government Semiconductor Advisory Panel and is an advisor to several chip startups.

Jalal was formerly the CEO of Dialog Semiconductor plc. Some of his professional highlights at Dialog include creating new design centers in seven countries; successfully negotiating a strategic licensing deal to transfer IP and resources to Apple Inc.; raising equity in European capital markets; acquiring and integrating several chip companies totaling over \$1bn.

Dr. Jalal led the company to become the world leader in Power Management products, used for all Apple devices, and led the expansion into new product lines for Connectivity (low power Bluetooth SIG/Wi-Fi), Power Conversion and Mixed Signal products for mobile consumer and IOT markets. He led the strategic M&A deal resulting in the sale of Dialog Semiconductor to Renesas Corporation of Japan in August 2021 for \$5.8bn.





#### JANET COLLYER

Board member at EnSilica, Quantum Dice, Machine Discovery, Aerospace Technology Institute UK Semiconductor Advisory Panel

Janet is Senior Independent Director at AIM listed <u>EnSilica</u>, Chair of the Board at two UK startups, <u>Quantum Dice</u> and <u>Machine Discovery</u>, and Independent NED at the <u>Aerospace Technology Institute</u>. She is also a member of the <u>UK Semiconductor Advisory Panel</u> that advises the government on its semiconductor strategy and helps steer future of the UK semiconductor sector.

She mentors women engineers as they progress through the ranks through to C-suite.

Prior to her portfolio career she held senior positions within the semiconductor industry including Strategic Group Director at Cadence Designs System leading international teams, driving bespoke approaches and effective ecosystem partnerships that delivered business revenue growth across the semiconductor supply chain.

After her Cambridge University engineering degree she did semiconductor design in USA and UK moving to roles in product management, project management, global services, IP and software sales director.





# JEREMY GLEESON Portfolio Manager & Head of Investment Team AXA Investment Managers

Jeremy Gleeson has been the lead manager on the Global Technology fund since joining AXA IM in 2007 and also the lead manager on the Digital Economy fund since its launch in 2017, alongside being the global technology specialist for the equity team. Prior to AXA IM, Jeremy was portfolio manager on several technology funds at Close Investments (formerly Reabourne Technology), a subsidiary of Close Brothers Group PLC. He started his investment career in 1997. Jeremy was named Tech Fund Manager of the Year at the UK Tech Awards in 2017.

Jeremy holds Master's degree in Systems Engineering from Cardiff University and a Bachelor's degree in Mathematics. He is also a CFA charterholder.





JODI SHELTON
Co-Founder & CEO
GSA

Jodi Shelton is the Co-founder and CEO of GSA. A pioneer within the technology space, Shelton was instrumental in the creation of the Alliance and has continuously addressed global issues in the industry as the voice of its members. Under her leadership, GSA has grown its membership to include companies throughout the supply chain representing 30 countries across the globe. In her role as president, Shelton oversees GSA daily operations, ensuring that the Alliance remains focused on achieving its defined mission and goal to foster a more effective ecosystem through collaboration, integration and innovation. She is the key liaison between GSA and the business community at high-level financial and industry conferences, providing insight on key topics pertaining to the global fabless supply chain.

Shelton earned her bachelor's degree in political science from San Diego State University and her master's degree in political science from the University of Houston.





MARIA MARCED
Former President, TSMC Europe
Board Member of Ceva, IQE, Sequans

Mrs. Maria Marced holds non executive Director positions at Ceva and IQE. Prior to this, Maria has been the President of TSMC Europe from November 2007 until December 2023 being responsible for the strategy and management of TSMC Europe and leading the decision of TSMC on the EU fab.

Maria joined Philips Semiconductors in 2003 as Senior Vice President and General Manager of the Connected Multimedia Solutions Business Unit and later on as General Manager of Sales and Marketing during the transformation of Philips to NXP. Maria joined Philips from Intel where she developed her professional career over 19 years, reaching the top position in the Europe, Middle East and Africa region as Vice President and General Manager. Mrs. Marced holds a degree in Telecommunications Engineering from Universidad Politecnica de Madrid, Spain. She was born in Valencia, Spain and is married with one daughter.





MATT GEHL

Managing Director, Head of EMEA
Technology Banking
JPMorgan Chase

Matt Gehl is Head of EMEA Technology Investment Banking at J.P. Morgan based in London. Prior to joining JPM, Matt spent the previous 7 years at Deutsche Bank where he ran their EMEA Technology Investment Banking team. Matt joined Deutsche Bank from Arma Partners, a Technology M&A boutique based in London and Palo Alto. Matt began his career at Goldman Sachs as a technology equity research analyst focused on the semiconductor and semiconductor equipment sectors in NY, Frankfurt and London.

Matt has a dual MBA from Columbia and London Business School and a BBA from the University of Wisconsin.





# NIRANJAN SIRDESHPANDE Global Head of Investment, M&G Catalyst Fund M&G Investment

Niranjan is the Global Head of Investments for the M&G Catalyst Fund and is based in London. The Catalyst Fund manages USD 6 billion in committed capital and invests in world-leading technology and lifesciences companies that create a positive impact for society and our planet. Niranjan has successfully executed transactions in diverse sectors such as Life-sciences, Deep-tech, Clean Energy, Enterprise SaaS, Fintech/Consumer Credit, Micro Finance, Aircraft Leasing, and Music Royalties, among others.

He also represents M&G on the Board and Investment committees of a number of portfolio companies.





ORESTE DONZELLA
EVP, EPC Business Group
KLA Corporation

Oreste Donzella serves as Executive Vice President of the Electronics, Packaging and Component (EPC) business group at KLA Corporation, which include multiple product divisions, targeting growth opportunities in specialty semiconductors, packaging, printed circuit board and display markets. Previously, Oreste was the Chief Marketing Officer (CMO) of KLA. In this role, he oversaw corporate marketing activities, market analytics and forecast, and company-wide collaborations with the broad electronics industry.

Prior to his CMO role, Oreste led the world-wide field applications engineering team, and was responsible for Customer Engagement projects and product portfolio optimization for wafer inspection platforms at KLA. Previously, Oreste was Vice President and General Manager of the Surfscan and SWIFT divisions at KLA-Tencor. In these positions, Oreste was responsible for the unpatterned wafer inspection, wafer geometry, and macro inspection business, overseeing new products development, sales, and marketing activities, customer support, and ultimately, division financial performance (P&L).

Oreste brings 30+ years of experience in the semiconductor industry. Prior to joining KLA in 1999, he spent more than six years at Texas Instruments and Micron Technology, holding engineering and management positions in the process integration and yield enhancement departments.

Oreste earned his master's degree in electrical engineering from the University La Sapienza in Rome, Italy.





PAUL WILLIAMSON
SVP and GM, IoT Line of Business
Arm

Paul leads the IoT line of business at Arm where he and his team are working alongside the Arm ecosystem to bring compute to a diverse set of applications, unlocking value with intelligence and automation.

Previously at Arm, Paul has led the client line of business, shaping the future of consumer products, as well as the security, IoT, and wireless businesses.

Prior to joining Arm, Paul led the low-power wireless division of CSR, a fabless semiconductor business (now part of Qualcomm). Paul started his career in engineering consultancy, working with leading global brands to develop innovative products and services. He holds an MEng from Durham University.





PETE HUTTON
Chairman
Agile Analog

Pete Hutton is Chairman of Agile Analog, Cambridge GAN Devices and Xampla, and an Angel investor and Advisor for several startups and scale-ups in Cambridge, Edinburgh, London, and Oxford.

His previous full-time role was President of Product Groups at Arm, where he was responsible for all product development, marketing and licensing. This covered 3,500 staff, in more than 25 global locations, and working closely with a range of global Partners. In his three years in the role, revenue increased by around 50% (> \$500m).

Prior to that, Pete held senior positions within other Arm divisions, ran corporate engineering for Wolfson, was general manager for processors at ARC and a group director for Cadence.

Pete understands long-term strategy, medium-term planning and the need to balance those with a focus on short-term execution. He is personally familiar with Software, Hardware and Intellectual Property Licensing.





PETER KUO
Industry Partner
Novo Tellus Capital Partners

With 20+ years of experience in global technology banking and extensive deal-making experience in China, Peter has worked on multiple cross-border transactions involving the US, Chinese and European capital markets.

Peter is a co-founding partner of Canyon Bridge, a \$1 billion private equity firm focused on technology transactions as well as the CEO of PTK Acquisition Corporation, an NYSE-listed special purpose acquisition corporation focused on technology acquisitions. Prior to founding PTK and Canyon Bridge, Peter was a managing director at Lazard, where he led the firm's coverage of the China technology and consumer sectors. He began his investment career at Pacific Venture Group, an Asiabased VC firm and also practiced law at Davis Polk & Wardwell. He has worked on transactions worth more than \$100 billion.

Peter holds a BSFS from Georgetown University, a JD from Harvard Law School and is a Fullbright Scholar.





PHILIPPE NOTTON
Founder & CEO
SiPearl

Philippe Notton is the CEO and Founder of SiPearl, the French company building the energy-efficient HPC dedicated microprocessor designed to work with any third-party accelerator (GPU, AI, quantum).

His original vision of SiPearl came in 2015 while he was leading a division of 2400 engineers at STMicroelectronics. In 2017, he joined Atos to set up the European Processor Initiative (EPI) consortium, which aimed to foster the return of high-performance microprocessor design to Europe. In June 2019, he created SiPearl with the support of the European Union. In October 2023, he won an emblematic contract to equip JUPITER, the first exascale supercomputer in Europe.





RUSS SHAW
Founder
Tech London Advocates &
Global Tech Advocates

Russ Shaw CBE is the founder of Tech London Advocates and Global Tech Advocates. He founded TLA in 2013, bringing together tech leaders to champion London as a global tech hub and campaign to address some of the biggest challenges facing tech companies in the UK.

Russ founded GTA in 2015, to support startups and scaleups in tech hubs around the world. The network with over 30,000 members has 33 agreements in place to support hubs in over 50 countries across six continents.

He is an angel and venture investor and has been non-executive director of many high growth businesses, including Dialog Semiconductor. In 2023, Russ became a co-founder the UK Chips Coalition – representing the industry voice on how to implement the UK semiconductor strategy announced by the UK Government.





SAQIB BHATTI

Minister for Tech and the Digital Economy /
Department for Science, Innovation and Technology
UK Parliament

Saqib Bhatti was appointed Parliamentary Under Secretary of State at the Department for Science, Innovation and Technology on 13 November 2023.

The minister is responsible for:

- 1. Online safety policy, including the National Security Online Information Team
- 2. Digital regulation, including the Digital Markets, Competition and Consumers Bill and Digital Markets Unit
- 3. Digital skills policy
- 4. Digital inclusion policy
- 5. Tech policy and market facing tech
- 6. International tech strategy, including the internet governance and standards
- 7. Economic security policy (relevant to tech), including semiconductor strategy
- 8. Digital identity policy
- 9. Supporting the delivery of HMG's AI programme in the House of Commons, including the AI Safety Institute





SEAN REDMOND

Managing Partner

Silicon Catalyst

Sean Redmond has nearly 40 years of experience in the semiconductor and software industries. Starting his career as a chip designer, he led two market leading design teams. Firstly in digital stereo sound for television and then the first embedded mobile phone chip with VLSI technology. Sean was VP Europe for Verisity Design Inc, taking them from a standing start to over 160 customer sites throughout Europe in 5 years. He instigated and led the strategic partnership with ARM, fueling its meteoric rise and successful IPO.

Following their acquisition by Cadence, Sean became VP & GM of Cadence EMEA, doubling its business in 3 years. As VP WW Sales and Marketing for ARC, he helped turn its cash flow positive, leading to the eventual sale to Synopsys. In 2010 he founded and has been the CEO of Vertizan Limited, the developer of the Vitaq Al-driven Test Automation tool suite.

He is Chairman of a UK semiconductor company, Cascoda Ltd. Sean has recently worked closely with the UK government on industrial digital strategy, co-chairing the ElecTech council and became a core member of the Secretary of State's industrial digital leadership team. Sean has a BSc(hons), MSc EE from Kings College, London and MBA from Henley Management College.





SEBASTIAN GÖKE
Associate Partner
McKinsey & Company

Sebastian Göke is an Associate Partner in McKinsey's global Semiconductor and Advanced Electronics practice. He serves semiconductor clients along the full value chain, incl. component and equipment players, IDMs, Foundries, OSATs, and customers with focus on Operations and on Strategy topics. He is co-leading the sustainability effort in McKinsey's semiconductor practice.

Sebastian holds a PhD in fluid dynamics from the Technische Universität Berlin. He has a master's degree in Physical Engineering Sciences from TU Berlin (Germany) and the University of Newcaste (Australia).





SIMON THOMAS
CEO
Paragraf

Simon is CEO of Paragraf, the company he founded in 2018. With a diverse background in physics, engineering and materials science, he spent many years in the fields of compound semiconductor materials, devices, and applications. Working in multiple scientific and technology disciplines led to the innovation of a novel manufacturing technique for graphene fabrication and subsequently Paragraf, today the only company in the world capable of producing graphene-based advanced material electronics at scale.

Simon is passionate about developing technologies that can solve today's enduring challenges and beneficially affect global society. He believes that new materials hold the key to transformational products that will deliver a brighter more sustainable future.





STEWART EDMONDSON
CEO
UK Electronics Skills Foundation

Stewart Edmondson has been the CEO of the UK Electronics Skills Foundation since 2015. He has a degree in Electronics and a wide range of engineering and training management experience in both the public and private sectors. He has been a Chartered Engineer for over 20 years and is a Fellow of the IET. As a volunteer, he has chaired their Professional Development Advisory Committee.

He is a Royal Academy of Engineering Visiting Professor of Employability, Professionalism and Ethics at Aston University.





TAMSIN HEATH
Deputy Director Economic Security
UK Department for Science, Innovation, and Technology

Tamsin Heath is Deputy Director Economic Security at the UK Department for Science, Innovation and Technology (DSIT), where she covers semiconductors, emerging technology and economic security for UK technology. She was formerly Deputy Consul General in Hong Kong, and covered for 8 months as Acting Consul General. Before Hong Kong she worked for the Dutch Development Bank as Head of Public Affairs.

A career diplomat and economist, Tamsin has also been posted in Beijing and Riyadh, covered as Charge d'Affaires in Yemen, and worked as Private Secretary to the Foreign Office Permanent Under Secretary. She also worked for HMT and RBS.





VAYSH KEWADA
CEO and Co-Founder
Salience Labs

Vaysh is CEO and Founder of Salience Labs. Salience Labs has developed an optical switch to enable high bandwidth connectivity between GPUs, TPUs and other AI accelerators. Their proprietary technology is the result of decades of research from University of Oxford and Munster.

Having worked in strategy and transformation for McKinsey & Company and as an entrepreneur in residence for Oxford Science Enterprises, Vaysh is well poised to bring Salience Labs to the forefront of Silicon Photonics and address the ever increasing demand for AI infrastructure."





WILL DRURY
Executive Director – Digital & Technologies
Innovate UK

Will is Innovate UK's Executive Director for the Digital & Technologies domain covering a range of technologies and industries including Quantum, Semiconductors, AI, Future Telecoms and Creative Industries to name just a few.

Will joined UKRI as Challenge Director for Driving the Electric Revolution in January 2020, a role he continues to hold. Previously he worked at engineering and environmental consultancy Ricardo where he was Global Technical Expert – Power Electronics. Prior to this he led the engineering delivery team for electric drives at Ricardo having previously worked at a micro start-up company in the renewable energy sector.

Will holds a PhD from the University of Bristol in Electrical Engineering, is a Chartered Engineer, Fellow of the IET and a Senior Member of the IEEE. Will is a visiting Professor at Strathclyde University.





YANKIN TANURHAN SVP, Engineering Group Synopsys

Dr. Yankin Tanurhan, Senior Vice President Engineering, Solutions Group at Synopsys is responsible for DesignWare Processor Cores, Security IP, Wireless Interface IP, Smart Subsystems and Non-Volatile Memory business units. The products in his portfolio include low power and high-performance ARC embedded CPUs, NPUs, DSPs targeting markets from Mobile, IoT, Embedded Vision, Al/ML, Digital Home, Automotive/Industrial, Security to Storage, ASIP tools with products like ASIP Designer and Programmer, IP Subsystems products like Sensor Fusion, Audio, Vision and Security Subsystems and CMOS based Non Volatile OTP and MTP memory IP blocks. His teams additionally develop a wide variety of Security IP from TRNGs to full blown HSMs and IDEs. Lately he extended the R&D activities into Bluetooth, Zigbee products.

Dr Tanurhan has authored 100+ papers in refereed publications. He holds a B.S. and M.S. in Electrical and Computer Engineering from Rheinisch Westfaellische Technische Hochschule (RWTH) in Aachen, Germany and a Dr. Ing. degree summa cum laude in Electrical Engineering from the University of Karlsruhe (TH) in Karlsruhe, Germany.



#### IN PARTNERSHIP WITH



Department for Science, Innovation, & Technology

#### PLATINUM SPONSOR



Established in 1987, TSMC is the world's first dedicated semiconductor foundry. Today, TSMC serves 532 customers and manufactures 12,698 products covering a variety of end markets including high performance computing, smartphones, the Internet of Things (IoT), automotive, and digital consumer electronics.

#### **GOLD SPONSOR**



Pragmatic is revolutionising semiconductor fabrication with ultra-low-cost, flexible integrated circuit technology that makes it quick and easy to embed intelligence almost anywhere. With rapid cycle times that substantially accelerate time to market, its foundry provides high-volume fabrication at a fraction of the cost of silicon, and with significantly lower environmental impact.

#### SILVER SPONSORS



Capgemini is a diverse technology partner helping organizations accelerate their transition to a digital, sustainable world. We create tangible impact for your enterprise and society.



KLA is a global technology leader. We make an impact by creating solutions that drive progress and transform industries. We provide leading-edge technology and devices using advanced inspection tools, metrology systems, and computational analytics. Our solutions accelerate tomorrow's electronic devices.

#### SILVER DINNER SPONSOR

#### JPMORGAN CHASE & CO.

JPMorgan Chase & Co. is one of the world's oldest, largest and best-known financial institutions. In a fast-moving and increasingly complex global economy, our success depends on how faithfully we adhere to our core principles: delivering exceptional client service; acting with integrity and responsibility; and supporting the growth of our employees.

#### **WLI SILVER SPONSOR**



At NXP, we build solutions—not just products—that enhance the capabilities of people, organizations and the world at large. We design <u>purpose-built, rigorously tested technologies</u> that enable devices to sense, think, connect and act intelligently to improve people's daily lives. We are committed to keeping our customers ahead of the competition and helping them to prepare for change.

#### **BRONZE SPONSORS**

#### arm

Born more than 30 years ago with the goal of designing a computer intended to run on a battery, Arm's DNA is built around power-efficient CPUs. Today, Arm compute platforms are the most power-efficient on the planet and continue to push the thresholds of performance to enable the next generation of smart, Al-capable, visually immersive, and increasingly autonomous experiences on everything from the tiniest sensors to the smartphone to the automobile and to the datacenter.



Silicon Catalyst is the world's only incubator focused on semiconductor solutions, including MEMS, sensors and intellectual property. We accelerate startups from idea through prototype, and onto a path to volume production. We have evaluated over 600 startups worldwide and have admitted 46 exciting companies. Silicon Catalyst's ecosystem provides everything our startups need to design, fabricate and market semiconductor solutions.

# SYNOPSYS<sup>®</sup>

Synopsys technology is at the heart of innovations that are changing the way people work and play. Self-driving cars. Machines that learn. Lightning-fast communication across billions of devices in the datasphere. These breakthroughs are ushering in the era of Smart Everything—where devices are getting smarter and connected, and security is an integral part of the design.

#### **WLI BRONZE SPONSOR**

# unec

Founded in 1984, imec quickly made its name as the leading research hub for <u>advanced CMOS scaling</u>. How? By enabling nano- and digital technology innovation with a significant impact on the quality of life, alongside our partners from companies, governments and academia.

#### **EXHIBITORS**

# **ALTAIR**

Since our founding in 1985, Altair has been making more possible for our customers. Now, with the applied convergence of AI, data analytics, HPC, and simulation, the definition of what's possible grows more ambitious every day.

Bolt Graphics is a silicon IP company delivering >10x faster graphics processors - focusing on ray tracing and expanding soon! At Bolt, we focus on designing hardware engines that are 10x faster and more efficient than general purpose solutions like GPUs and CPUs.



CircuitSutra was started in 2005 with an aim to define next generation methodologies that have the potential to transform the Semiconductor design and Electronics system design. We have seen the evolution of SystemC based ESL methodologies from the research phase to pilot projects to main stream adoption, and we have actively contributed in this journey.



Marvell is trusted by the world's leading technology companies for 25 years, we move, store, process and secure the world's data with semiconductor solutions designed for our customers' current needs and future ambitions. Through a process of deep collaboration and transparency, we're ultimately changing the way tomorrow's enterprise, cloud, automotive, and carrier architectures transform—for the better.



Pragmatic is revolutionising semiconductor fabrication with ultra-low-cost, flexible integrated circuit technology that makes it quick and easy to embed intelligence almost anywhere. With rapid cycle times that substantially accelerate time to market, its foundry provides high-volume fabrication at a fraction of the cost of silicon, and with significantly lower environmental impact.